High-Vacuum Particulate-Free Deposition of Wafer-Scale Mono-, Bi-, and Trilayer Molybdenum Disulfide with Superior Transport **Properties**

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Supporting Information

ABSTRACT: Wafer-scale MoS₂ growth at arbitrary integer layer number is demonstrated by a technique based on the decomposition of carbon disulfide on a hot molybdenum filament, which yields volatile MoS_x precursors that precipitate onto a heated wafer substrate. Colorimetric control of the growth process allows precise targeting of any integer layer number. The method is inherently free of particulate contamination, uses inexpensive reactants without the pyrophoricity common to metal-organic precursors, and does not rely on particular gas-flow profiles. Raman mapping and photoluminescence mapping, as well as imaging by electron microscopy, confirm the layer homogeneity and crystalline quality of the resultant material. Electrical characterization revealed microampere output current, outstanding device-to-device consistency, and exceptionally low noise level



unparalleled even by the exfoliated material, while other transport properties are obscured by high-resistance contacts typical to MoS₂ devices.

KEYWORDS: chemical vapor deposition, transition metal dichalcogenides, wafer-scale growth, transport noise measurements, colorimetry

INTRODUCTION

Mono- and few-layer transition metal dichalcogenide (TMD) films, particularly MoS₂ and WSe₂, are promising candidate materials in the continuing miniaturization of semiconductor devices. They combine a robust well-defined semiconducting behavior with appreciable processing stability.^{1–3} Indeed, it has been shown that at the monolayer limit, they offer unparalleled on-off ratio,⁴ subthermionic tunnel field-effect transistor (FET) performance,⁵ and unique electronic and optical properties.⁶⁻⁹ The wide band-gap of few-layer MoS₂ makes it promising for high-temperature applications.¹⁰ It has also been demonstrated that MoS_2 is a versatile sensor material.^{11,12} While semiconductor devices conventionally utilize the substrate semiconducting material as the transistor channel, introduction of vertical cell access transistors called for by the International Technology Roadmap for Semiconductors in 2019 requires the deposition of high-quality channel materials after initial patterning steps. Here, two-dimensional (2D) van der Waals (vdW) materials offer the advantage of very shallow channels at reduced surface scattering. Previous methods to produce wafer-scale monolayer TMD films have included tubefurnace chemical vapor deposition (CVD),¹³⁻¹⁹ liquid-phase exfoliation,20-22 and metal-organic CVD or atomic layer

deposition.²³⁻³⁰ These techniques utilize chloride, sulfide, or oxide-based transition metal precursor powders or high-vaporpressure carbonyls/metal-organic compounds. An ideal deposition method produces high-quality homogeneous films at arbitrary integer layer number and wafer-scale coverage without the use of any powder or pyrophoric precursor to avoid particulate contamination and processing hazards. Here, we show the growth of wafer-scale continuous MoS₂ films by a process that fulfills all of these requirements and yields a material superior in current noise characteristics to exfoliated and powder CVD films.

Our process departs significantly from the prior work by the use of a set of metallic molybdenum filament wires (Alfa Aesar) as the molybdenum source. Figure 1a shows a schematic of our growth setup. The method utilizes the difference in volatility between molybdenum metal (melting point: 2623 °C) and MoS₂ (melting point: 1185 °C) as well as other MoS_r species; this ensures that exclusively MoS_r species are released from the filaments. Consequently, the material

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Figure 1. (a) Schematic of the high-vacuum growth process utilizing a set of directly heated molybdenum filaments and carbon disulfide as the chalcogen source. Volatile MoS_x precursors are formed at the filament and precipitate onto the heated substrate coalescing into a continuous and crystalline film. Inset: photograph of heated filaments; (b) photograph of substrates before and after single-layer MoS_2 growth.



Figure 2. Hue (0-255) of the Mo-rod reflection (top in the left inset) during film deposition. The hue curve reveals the completion of layers, for which Raman spectra are shown in the right inset.

deposited onto the substrate is already of the correct elemental composition and sufficiently surface-mobile to arrange itself into large grains and, ultimately, a continuous film. A background of carbon disulfide gas at a pressure of 6×10^{-4} Torr (compared to a base pressure of the reactor of 1×10^{-7} Torr) is used as a sulfur source for the formation of MoS_x species on the filaments and to assure the stoichiometry in the resultant film. At a process time of up to 60 min depending on the layer number, a continuous and homogeneous single-layer film of MoS₂ is deposited at the wafer scale (Figure 1b). A number of prior studies^{31–33} have addressed the CVD

A number of prior studies^{31–33} have addressed the CVD growth of MoS_2 single layers and found a sequence of nucleation and in-plane aggregation. Figure 2a illustrates how we control the film growth: light from the hot molybdenum filaments (inset in Figure 1a) is reflected from the wafer substrate during growth. As CS_2 molecules decompose at the molybdenum filaments, MoS_x precursors evolve, evaporate from the rods, and deposit onto the substrate where they obtain stoichiometric sulfur contents from the environmental CS_2 and form into extended islands—or evaporate again, given the high platen temperature of 650 °C. In a balance between incoming MoS_x species and evaporation of the material, the film ripens into larger and larger crystalline islands. The growth rate is highest when a sizable number of large grains have formed that are stable from evaporation and offer edge sites for growth; it is lowest whenever a layer is complete.

In this work, we employ concomitant measurement of the hue of the specular-reflected Mo-filament light from the substrate for process control. This approach is novel for process control, but the dependence of the substrate color on single- and few-layer films has been well understood. Blake et al. described the contrast and color dependence of graphene on different thickness SiO_2 layers on Si using a multiple internal reflection method.³⁴ This description has since been expanded, for instance by Zhang et al.,³⁵ to reveal wavelength-/ color-dependent amplification in MoS₂ imaging and spectros-copy.

RESULTS AND DISCUSSION

Figure 2 shows the hue value of the light from the molybdenum filament reflected on the substrate as the film is growing layer by layer. The hue value is obtained by pointing a video camera from above and through the gaps of the glowing molybdenum rods at the sample during growth. Translation of the camera's red-green-blue-encoded signal into the common hue–saturation–brightness format yields the hue value, which cycles through the color circle from 0 to 255. The inset in Figure 2a shows the camera view on the filament in the bottom and its reflection from the substrate in the top. Numeric color temperature correction based on the albedo of the filament (bottom) has the power of removing dependence of the hue signal of the reflection (top) from the filament temperature but is not commonly required for the growth.

The main graph of Figure 2 shows the evolution of the hue value during growth. Initially, the growth is slow because of the absence of growth nuclei and rapid re-evaporation of the material into vacuum. As the growth proceeds, more stable island perimeter becomes available and it speeds up, just to get slower again, when the first layer is about to complete and the available island perimeter to attach to becomes smaller. This is visible as the first inflection on the hue curve. The growth

stopped at this point shows a single-layer Raman signal, as shown in the inset on the right.

If the growth process is continued at the unchanged filament temperature, substrate temperature, and CS_2 gas concentration, then the second layer nucleates, increases in size, speeds up in growth, and the growth decelerates again once it nears completion. By stopping the growth at this point, a bilayer film is produced with the corresponding Raman signal (inset). If the film growth is continued, the trilayer color (inset) is reached and ultimately a thick film emerges.

In order to achieve the highest quality films, the actual growth process proceeds not at constant filament brightness as used in the experiment of Figure 2. Rather as each layer nears completion, the filament temperature is reduced gradually to slow down the growth and allow optimal ripening/arrangement of the interisland/domain boundaries as well as evaporation of any adlayer islands that may have formed during the growth process. The Supporting Information (Figure S6) shows a scanning transmission electron microscopy (STEM) image that corroborates attachment between adjacent grain boundaries, which is important for film utility. If another layer is desired, the filament temperature is increased again to speed up the film growth and reduced again toward the completion of the next layer.

The Raman spectra in the inset of Figure 2 show the E_{2g}^1 inplane and A_{1g} out-of-plane peaks for different layer thicknesses. The peak separations are 19 (1L), 22 (2L), 23 (3L), and 25 (bulk) cm⁻¹. The separations match well the literature values for mono-, bi-, trilayer and bulk MoS₂.^{6,7}

Mapping of the Raman E_{2g}^1 and A_{1g} peak separation confirms the homogeneity of the material. Figure 3a-c shows



Figure 3. (a-c) Spatial mapping of the separation of the in-plane (E_{2g}^1) and out-of-plane (A_{1g}) modes of single-, bi-, and trilayer films, respectively. The values of 19.5, 22, and 23 cm⁻¹ are tell-tale of the respective film thickness. (d) Trace of the Raman peak separation in steps of 840 μ m across 1 in. of a wafer substrate. (e) Extended range of the Raman spectrum for the single-layer MoS₂ film.

millimeter-scale maps of the peak separation; at each layer thickness, the variation is well below one wavenumber. The isolated white and black dots are caused by the dust deposited onto the sample during Raman mapping in ambient unfiltered air and are not a consequence of the growth process. In order to ascertain the homogeneity of the film over a large range, we took Raman spectra at 840 μ m separation across a wafer refocusing the beam at each location. Figure 3d shows the

results and affirms the large range homogeneity of the wafer beyond the millimeter-scale map areas of Figure 3a-c.

Despite the use of carbon disulfide as a precursor, we find no Raman signature of a carbon film (Figure 3e). The presence of strong photoluminescence (PL) (as strong as on exfoliated samples) is a further indicator of the absence of a carbon film. X-ray photoelectron spectroscopy measurements of the substrate before and after transfer of an MOS_2 film show no significant difference of carbon trace contamination.

The PL is an indicator of the film quality and sensitive to adsorbed contaminants, strain, and other extrinsic effects. A typical PL spectrum of the film is shown in Figure 4a. The A exciton is found at 1.89-1.90 eV, slightly blue-shifted compared to conventional CVD and exfoliated materials.³⁶ We attribute this to strain induced by the higher thermal expansion coefficient of the SiO₂ substrate as compared to the MoS₂ film, leading to compressive strain during cooling down from growth temperatures, as is to be expected for a continuous film. Compressive strain of MoS₂ is typically associated with a blue shift of the PL signature, as observed here.³⁷ Occasionally, we find across the wafer (on the millimeter scale) strain relief patterns manifested as a slight shift (<0.03 eV) of the PL peak position (Figure S3). This finding ascertains the mechanical continuity of our film at the wafer scale. The full width at half-maximum of the PL peak is consistently at an excellent value of ~0.077 eV. The PL brightness of the films rivals the best exfoliated and CVD samples we have produced. The B exciton is barely visible at 2.06 eV, further confirming the material quality. Mapping of the PL peak position across the sample shows variations on the single-digit millielectronvolt (meV) scale only (Figure 4b).

SEM images of the MoS_2 single-layer film are featureless at any length scale (Figure S5). The atomic force microscopy root-mean-square surface roughness is at 0.2 Å slightly better than that of the bare SiO₂ substrate before the growth. Transfer of the film onto a grid for STEM reveals a highly ordered array of MoS_2 unit cells at the expected periodicity (3.31 Å) of the 2H phase (Figure 4c). Extended areas of singlecrystalline domains are surrounded by a band of a number of very small (~10 nm) domains, validating the growth mode described above. The Supporting Information (Figure S6) shows that adjacent extended domains are chemically connected through the small domain band surrounding them, as is important for transport applications.

Appealing transport properties of the film are crucial for the technological application of the method. Electron beam lithography was used to fabricate 50 FET test structures using the oxide of the growth substrate as the gate dielectric (bottom-gated without transfer). It is important to highlight that our growth process is sufficiently benign to the substrate that oxide pinholes resulting in gate leakage are absent. We use a device geometry that includes the channel length/width of $0.25/20 \,\mu m$ (see insets in Figure 5a for the optical image of the device). The electrodes were fabricated from a stack of 5 nm of yttrium for adhesion and 50 nm of gold. At source-drain voltage $V_{\rm sd}$ below 0.5 V, the output current voltage characteristics (source-drain current I_{sd} as a function of the source-drain voltage V_{sd}) were linear and symmetric, indicating near-Ohmic properties of the drain and source contacts. Figure 5a shows the measured output characteristics at high drain voltage up to 4 V. This biasing condition corresponds to the 160 kV/cm electric field in the channel and power density dissipated by the channel of the order of $10^9 \text{ W}/$



Figure 4. (a) PL spectrum of a single-layer MoS_2 film. (b) Mapping of the A-exciton position across 1×1 millimeter of the sample area reveals variations well below 10 meV (full color scale). (c) STEM image of the crystalline structure of the film. The inset shows the Fourier transformation and two indexed peaks.



Figure 5. (a) I-V characteristics of the single-layer MoS₂ film at different gate voltages. Inset of an optical image of the device structure. The scale bar is 10 μ m. (b) Source-drain current as a function of gate voltage for a single-layer MoS₂ device. (c) Noise power as a function of frequency. (d) Box and whisker plots of the variation of I_{sd} at fixed V_g and for a set of $V_{sd\nu}$ normalized to the average I_{sd} at the respective $V_{sd\nu}$. Data are collected from 47 devices evenly spread across >1 cm² of the substrate area.

cm³. The reliable operation of mass-fabricated devices at these extreme conditions indicates the high quality of the material. It is known that the temperature dependence of the current voltage characteristics in this type of MoS_2 transistors is governed by two competing mechanisms: decrease of the threshold voltage and decrease of the mobility with the temperature increase.¹⁰ The superlinearity of the characteristics in Figure 4a can be naturally explained by the Joule heating, causing a decrease of the threshold voltage and a small degradation of the mobility.

Figure 5b shows the measured current as a function of applied gate voltage at fixed source-drain biases. The characteristics in Figure 5b do not show any trend of saturation at high gate voltage and are quite typical for bottom-gated MoS_2 devices in the absence of engineered contacts, such as edge contacts or the use of graphene as a contact material. The low apparent mobility in these devices on the order of 1 can directly be attributed to the common challenge of contacts to MoS_2 devices rather than indicating a property of the film; we expect that integration of top-/dual gating and engineered contacts would show the full potential of this material.³⁸⁻⁴⁵

The level of the low-frequency noise is an important metric of the material quality and suitability for electronic applications.⁴⁶ It is particularly important for 2D materials

where conduction electrons in the device channel are ultimately exposed to various defects in the gate dielectric and top surface.⁴⁷ The level of the low-frequency noise can be used to assess the concentration of defects and material tolerance to electromigration. In practical applications, the low-frequency noise defines the phase noise in communication systems and sets the limits for sensor sensitivity and selectivity. The low-frequency noise was measured in the frequency range of 1-50 Hz at room temperature. The devices were biased in a common source mode. Details of our noise measurement procedures have been reported by some of us elsewhere.⁴⁸ Figure 4c shows the normalized noise spectral density $S_{\rm I}/I_{\rm ds}^2$ as a function of frequency for a representative MoS_2 device. The noise is of clear 1/f type (*f* is a frequency). The absence of the generation-recombination bulges indicates that there are no high concentrations of any specific defect, which would dominate the low-frequency noise response. The noise spectral density reveals scaling with the applied gate bias, that is, carrier concentration in the channel, expected for semiconductor devices.⁵¹ The 1/f noise level in FETs is often characterized by the noise amplitude normalized to the channel area.⁵² The minimum noise level measured in studied devices was of the order $2 \times 10^{-7} \text{ Hz}^{-1} \,\mu\text{m}^2$. This is smaller than that reported in earlier publications for MoS₂ transistors implemented with the exfoliated or CVD material.49,53 The latter provides an

additional confirmation of the material quality from the device applications' point of view.

In order to demonstrate the homogeneity of the film, we fabricated a set of 50 devices evenly spaced across more than 1 cm² of the substrate area. Of the 50 devices fabricated, one had a failure of the electron beam during writing and two were lithographically nonviable. Figure 5d shows a box and whisker plot of the variation of I_{sd} at different V_{sd} across all remaining 47 devices. We find a maximum variation by a factor of 2 and an interquartile range of 0.8–1.3 with 1 normalized to the average current at any V_{sd} .

CONCLUSIONS

In conclusion, the scalable growth of continuous single- and few-layer MoS_2 is demonstrated using a high-vacuum deposition process. Optical and electrical characterizations validate high quality and homogeneity of the material as well as superior noise performance. We highlight that these results are achieved using an amorphous SiO_2 substrate and without any powder or metal–organic precursors. Using the color of light reflected from the substrate, process control at the single-layer limit is achieved.

METHODS

High-Vacuum Growth. The MoS₂ growth reactor is an ultrahighvacuum system equipped with a turbo pump, ion gauge, and so forth for facile preparation of high vacuum. It houses a 3 in. hot-plate style heater (MeiVac) and a leak valve for the introduction of CS_2 based on its native vapor pressure. We typically use silicon wafers with 300 nm dry oxide SiO₂ (WaferPro). While at a base pressure of 1×10^{-7} Torr, the sample is heated to 650 °C. The CS_2 leak value is opened to increase the chamber pressure to 6×10^{-4} Torr. Six separate molybdenum wires, each 4 in. long with a diameter of 0.5 mm and a separation of 1 in., provide the molybdenum source. The molybdenum filaments are resistively heated until they are approximately 1800 °C for up to 60 min, while the total filament power is controlled. The growth is monitored by colorimetric measurements using a standard microscope camera (OMAX). Using thresholding of the brightness, the reflection from the filaments is identified and the local average hue value is calculated. During the initial 10 min of the growth period, the filament power is pushed to reach the maximum filament temperature of 1800 °C for quick nucleation; as the single layer nears completion, the filament power is dropped and held steady to complete each layer before the next is nucleated. Subsequent layers can be formed by repeating the same process. After the filaments are switched off, the substrate heater is allowed to cool naturally. Once it reaches 200 °C, the CS₂ flow is stopped, the chamber is vented with nitrogen, and the wafer is removed.

Optical Characterization. The samples were analyzed with Raman and PL spectroscopy using a HORIBA LabRAM instrument with a 532 nm excitation laser. The Raman and PL measurements used 1800 and 600 gratings, respectively.

Device Fabrication. After the material is grown, C5 PMMA is spin-coated onto the sample at 4000 rpm for 45 s and is then baked at 180 °C for a \sim 600 nm thick layer of resist. The electrodes and pads are patterned onto the sample using electron beam lithography. Immediately after patterning, the sample is developed in 1:3 MIBK to IPA and IPA for 10 and 20 s, respectively. After the pattern is developed, the electrode material is deposited using electron beam evaporation of Y/Au (5 nm/50 nm). The sample is placed into acetone for 20 min to lift off the residual metal.

Electron Microscopy. MoS_2 for the SEM image was grown on a bare silicon substrate that was cleaned using a buffer oxide etch solution. Imaging proceeded in FEI XL30-FEG SEM. The TEM samples were prepared by using KOH solution to transfer the film to a TEM grid. The samples were analyzed using FEI Titan Themis 300.

The fast Fourier transform (FFT) of the material was obtained using the ImageJ FFT analysis of the TEM image provided.

ASSOCIATED CONTENT

S Supporting Information

The Supporting Information is available free of charge on the ACS Publications website at DOI: 10.1021/acsami.8b10857.

Photograph of the growth chamber and further optical, spectroscopic, and electron microscopy characterization of the films (PDF)

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Notes

The authors declare no competing financial interest.

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